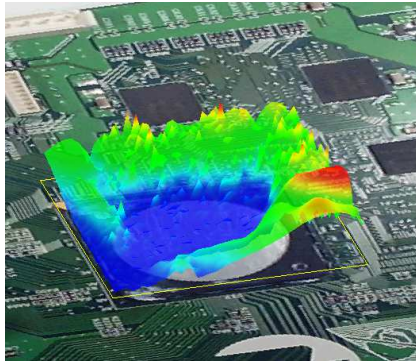


A One-Week Training in Electromagnetic Compatibility of Integrated Circuits

Sept 23-27, 2019



A unique practical course to take part to a global integrated circuit emission and susceptibility reduction strategy

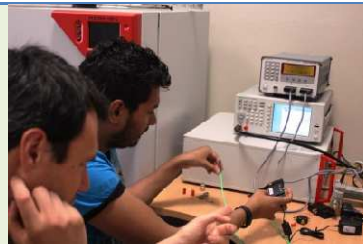
Audience

EMC engineers, IC users, IC designers, PhD students in electronics, researchers in electronics, Master students in Electronics & Embedded systems

Description of the Course

A quality-labeled **five-day course** focused on electromagnetic compatibility of integrated circuits is proposed by INSA Toulouse, France:

- **Basic concepts:** trends, influence on IC technology on EMC, specific units, impedance, margins, etc.
- **Parasitic emission**, with focus on IEC 61967 measurement methods
- **Susceptibility**, with focus on IEC 62132 measurement methods
- **Modeling** for predicting EMC (IEC 62433), based on standards such as IBIS, ICEM and ICIM
- **EMC guidelines** for improved emission and immunity to interference.



Practical sessions

Afternoons are dedicated to practical sessions including an access to the EMC laboratory of INSA Toulouse, for hands-on experiments of IC emission and immunity characterization.

Illustrations of all concepts are made using IC-EMC www.ic-emc.org, freeware including unique features and tools for efficient EMC simulations of integrated circuits.

Lecturers



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More information: www.ic-emc.org > Trainings
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